ç

Appin. No. 09/890,550 Amendment Reply to Office Action dated November 27, 2002

Docket No. 2000-22

FAX RECEIVED

MAY 2 8 2003

AMENDMENTS TO THE CLAIMS

GROUP 1700

This listing of claims will replace all prior versions, and listings, of claims in the application:

1-27. (Cancelled)

28. (Withdrawn)

29-40. (Cancelled)

41-42. (Withdrawn)

43. (Currently amended) A wafer grinder table having a grinding surface for grinding a semiconductor wafer held on a wafer holding plate, the table including comprising:

a plurality of base materials, each of which is a ceramic-metal composite formed by impregnating metal silicon in opened bores <u>pores</u> of a porous body made of silicon-containing ceramic;

a bonding layer formed from the metal silicon to bond the base materials; and a fluid passage formed in a bonding interface of the base materials.

44. (Previously added) The wafer grinder table according to claim 43, wherein, in the ceramic-metal composite, the porous body includes silicon carbide crystals with an average grain diameter of 20μm to 100μm, has a porosity of 10% to 50%, and has a thermal conductivity of 160W/m*K or more, and wherein 100 parts by weight of silicon carbide is impregnated with 15 parts by weight to 50 parts by weight of the metal silicon.

45. (Currently amended) The wafer grinder table according to claim 44 <u>43</u>, wherein the silicon carbide crystals include 10vol% to 50 vol% of fine silicon carbide crystals, which have an average grain diameter of 0.1μm to 1.0μm and 50vol% to 90vol%

(WP135042;2)

6

7

1

2

3

4

5

1

2

3

Appln. No. 09/890,550 Amendment Reply to Office Action dated November 27, 2002

Docket No. 2000-22 FAX RECEIVED

MAY 2 8 2003

- 4 of rough silicon carbide crystals, which have an average grain diameter of 25μm to OUP 1700
- 5 150μm.

1

2

5

1

2

3

4

5

6

1

2

3

4

5

6

- 46. (Previously added) The wafer grinder table according to claim 43, wherein the bonding layer has a thickness of 10μm to 1500μm.
- 47. (New) A wafer grinder table having a grinding surface for grinding a semiconductor wafer held on a wafer holding plate, the table including comprising:
- a plurality of bonded base materials, each formed from a silicon carbide-metal composite; and
 - a fluid passage formed in a bonding interface of the base materials.
- 48. (New) The wafer grinder table according to claim 47, wherein the silicon carbide-metal composite has a porous structure formed by silicon carbide crystals that includes opened pores, wherein the opened pores are impregnated with metal, wherein the silicon carbide crystal average grain diameter of 20µm or greater, a porosity of 30% or less, and a thermal conductivity of 160W/m*K or more, and wherein 100 parts by weight of silicon carbide is impregnated with 15 parts by weight to 50 parts by weight of metal.
- 49. (New) The wafer grinder table according to claim 47, wherein the silicon carbide-metal composite has a porous structure formed by silicon carbide crystals that includes opened pores, wherein the opened pores are impregnated with metal, wherein the silicon carbide-metal composite has a silicon carbide crystal average grain diameter of 20μm to 100μm, a porosity of 5% to 30%, and a thermal conductivity of 160W/m*K or more, and wherein 100 parts by weight of silicon carbide is impregnated with 15 parts by weight to 50 parts by weight of metal.

7